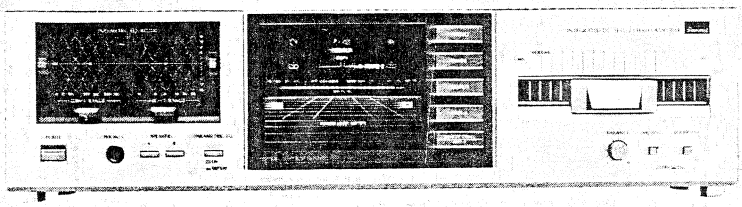
SERVICE MANUAL

INTEGRATED DC SERVO STEREO AMPLIFIER

SANSUI A-1100P

(Silver & Black Model)

CLASSIQUE A-1130P



CAUTION

1. Parts identified by the ⚠ symbol on the schematic diagram and the parts list are critical for safety. Use only replacement parts that have critical characteristics recommended by the manufacturer.
2. Make leakage-current or resistance measurements to determine that exposed parts are acceptably insulated from the supply circuit before returning the appliance to the customer.

Sansui

SANSUI ELECTRIC CO., LTD.

•SPECIFICATIONS

A-1100P

Power output
Min. RMS, both channels driven, from 20 to 20,000 Hz with no more than 0.05% total harmonic distortion
100 watts per channel into 8 ohms
Load impedance..... 8 ohms
Total harmonic distortion... less than 0.05% at or below rated min. RMS power output
Frequency response (at 1 watt)
..... 5 to 60,000 Hz
+1 dB, −2 dB
RIAA curve deviation (PHONO, 20 Hz to 20 kHz)
..... +0.5 dB, −0.5 dB
Input sensitivity and impedance
(1 kHz, for rated power output)
PHONO..... 2.5 mV/47 kilohms
(Max. input capability; 120mV at 1 kHz, less than 0.1% total harmonic distortion)
TUNER, TAPE PLAY.... 150 mV/47 kilohms
Output level (1 kHz)
TAPE REC..... 130 mV into 47 kilohms
Signal to noise ratio (short-circuit, A-network)
PHONO..... 72 dB
TUNER, TAPE PLAY.... 95 dB
Parametric equalizer
Equalizer frequency.... 70 Hz to 700 Hz
1 kHz to 10 kHz
Level variation range... ±15 dB
Power requirements
Power voltage..... 120/220/240V (50/60 Hz)
For U.S.A. & Canada
..... 120V (60 Hz)
Power consumption.... 350 watts 450 VA Rated
Dimensions..... 430 mm (16-15/16") W
112 mm (4-7/16") H
250 mm (9-7/8") D
Weight..... 7.0 kg (15.4 lbs.) net
7.6 kg (16.8 lbs.) packed

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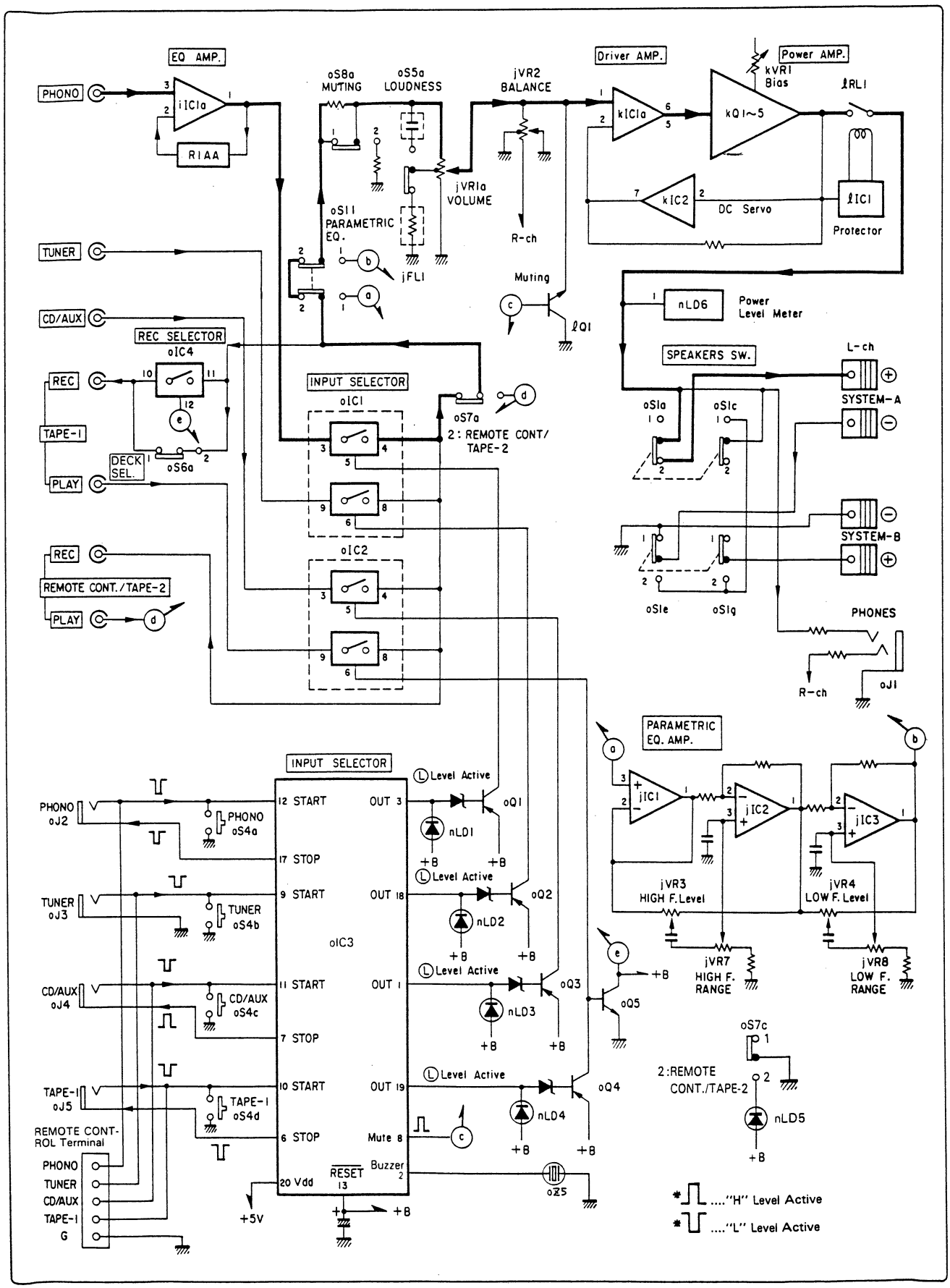
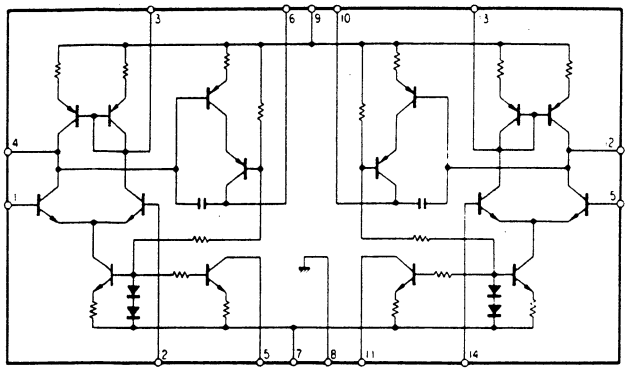
* Design and specifications subject to changes without notice for improvements.
* In order to simplify the explanation illustrations may sometimes differ from the originals.

1. BLOCK DIAGRAM

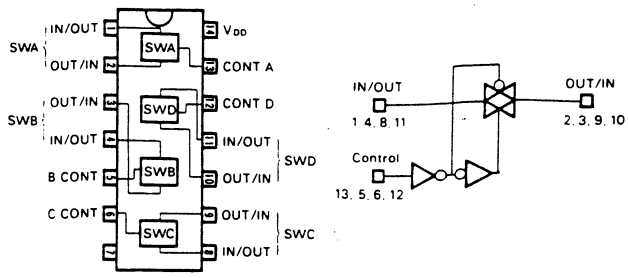


2. INTERIOR BLOCK DIAGRAM OF IC

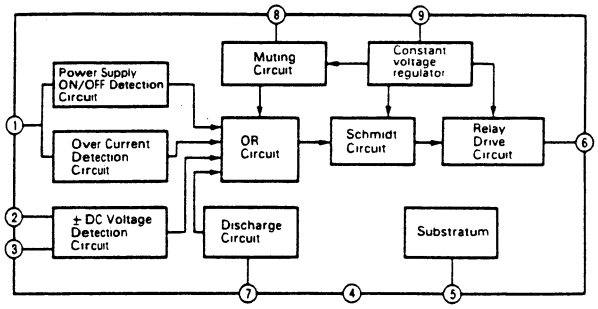
•STK3102-2A (Driver Amp. IC)



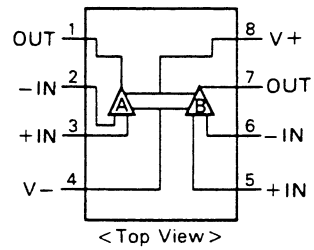
•LC4066BH/LC4966 (Quad Analog SW. IC)



•TA7317P (Speaker Protector IC)

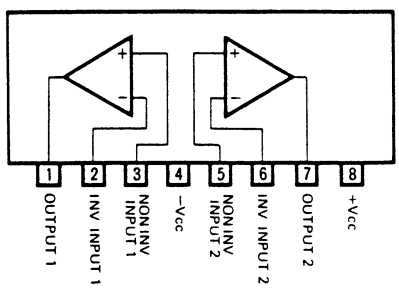


•NJM4558D (OP Amp. IC)



<Top View>

•M5218L (OP Amp. IC)



3. ADJUSTMENT (See Top View on page 9)

Note: 1) Master Volume .... Minimum
2) Room Temperature .... 18°C ~ 28°C (65°F ~ 83°F)
3) For this adjustment, run the unit for more than 5 minutes after the power is switched ON.
4) Before turning ON power switch, set kVR1 on F-4762 to center position.

STEP	SUBJECT	MEASURE OUTPUT	ADJUST	ADJUST FOR	REMARKS
1.	Bias Current Adj. L-CH	Test Terminal (TP) <Between emitters of power transistors (kQ4L & kQ5L)>	kVR1, L-ch (F-4762)	DC 10mV ± 2mV	•This bias value is converted from current value into voltage by ohms law.
2.	Bias Current Adj. R-CH	Test Terminal (TP) <Between emitters of power transistors (kQ4R & kQ5R)>	kVR1, R-ch (F-4762)	DC 10mV ± 2mV	

6. SCHEMATIC DIAGRAM

*Design and specifications subject to change without notice for improvement.
*La présention et les spécifications sont susceptibles d'être modifiées sans préavis par suites d'améliorations éventuelles.
*Änderungen, die dem technischen Fortschritt dienen, bleiben vorbehalten.